

Notice of References Cited	Application/Control No. 10/603,055	Applicant(s)/Patent Under Reexamination BLASI ET AL.	
	Examiner Mary C. Jacob	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,671,843	12-2003	Kebichi et al.	714/733
*	B	US-6,658,633	12-2003	Deyins et al.	716/5
*	C	US-6,353,904	03-2002	Le, Dai Minh	714/726
*	D	US-5,369,604	11-1994	Ravindranath et al.	702/119
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Zarrineh et al, "Automatic Generation and Validation of Memory Test Models for High Performance Microprocessors", Proceedings of the 2001 International Conference on Computer Design, pages 526-529, 23-26 Sept, 2001
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.